

FIG. 2

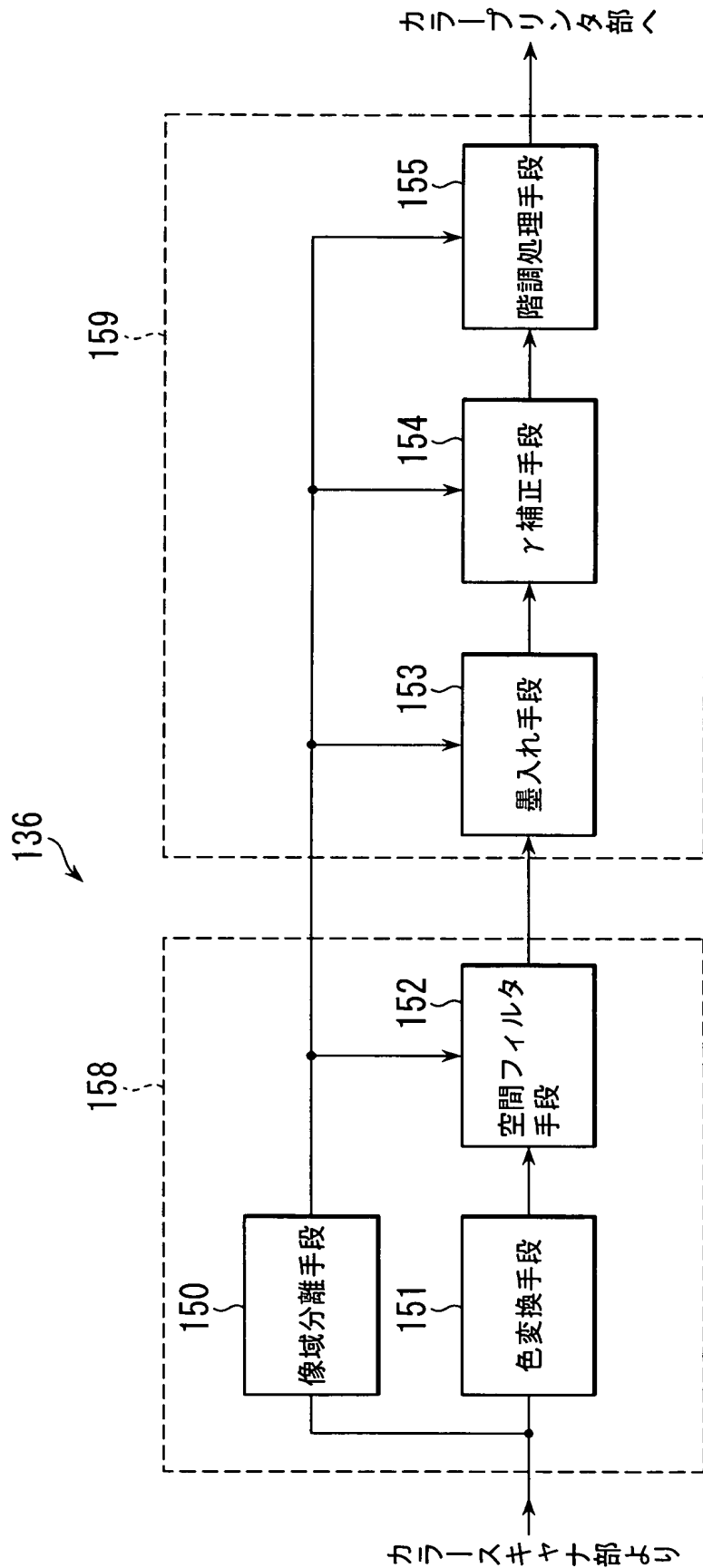
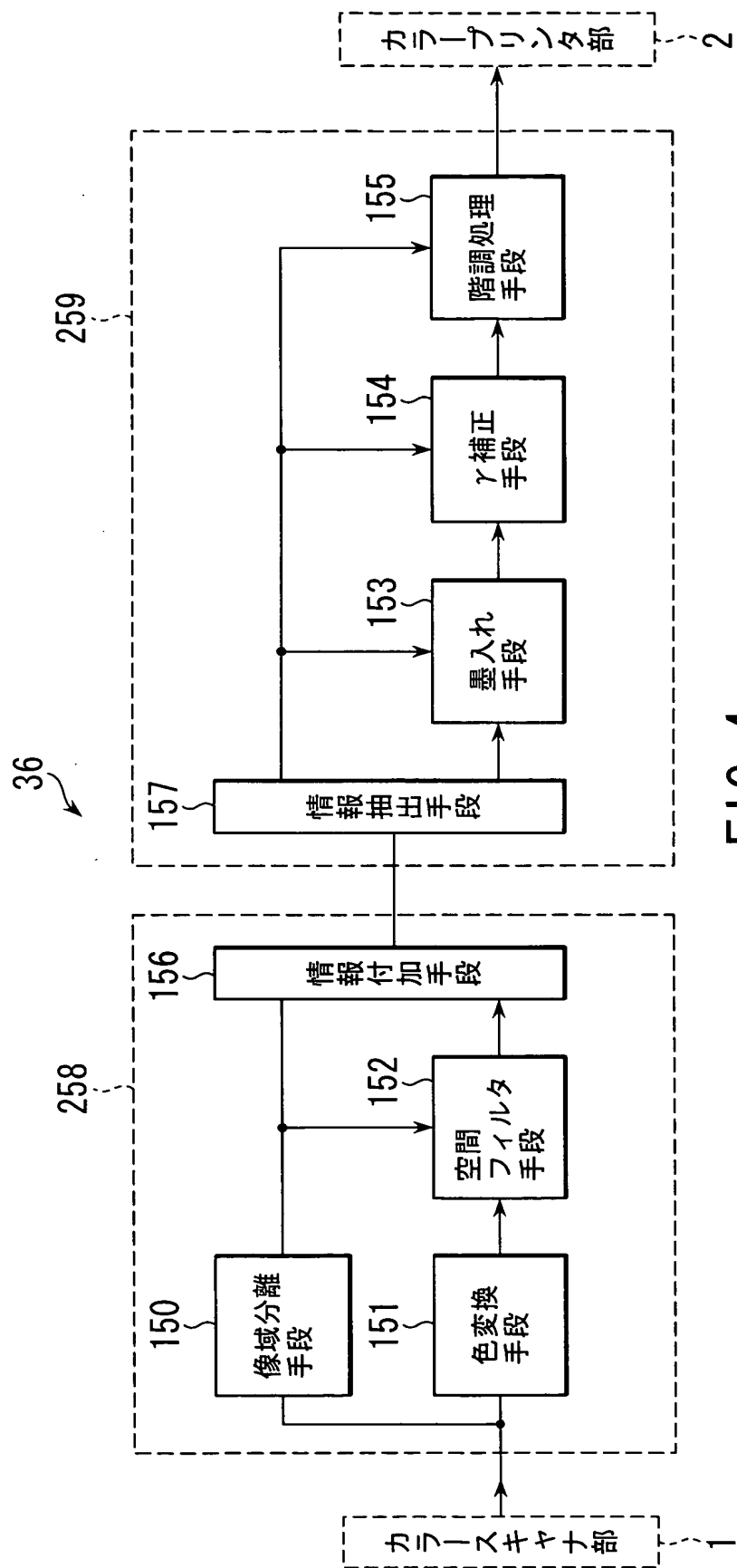


FIG. 3



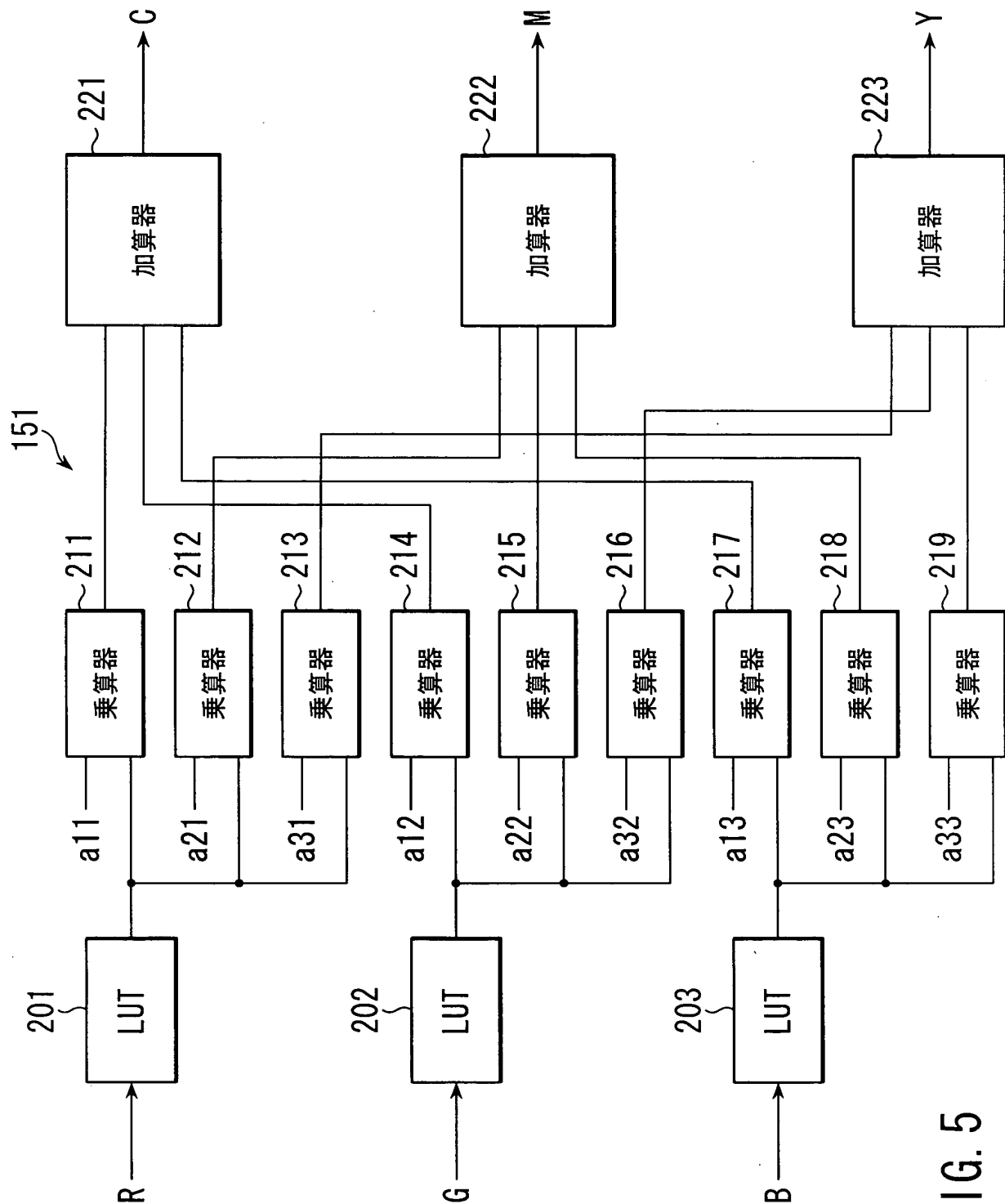


FIG. 5

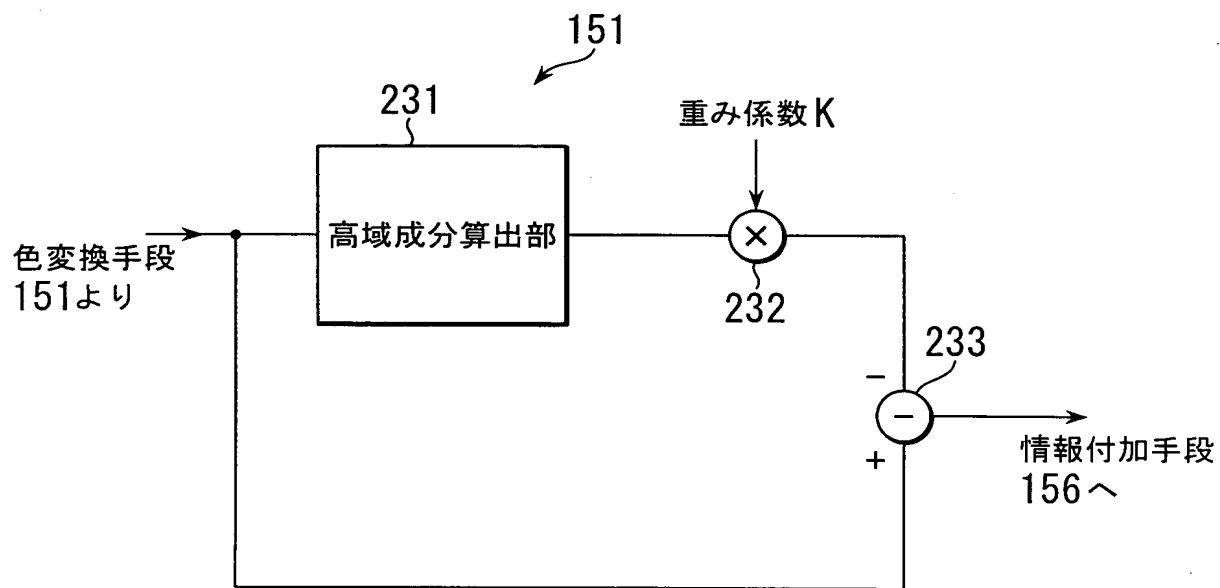


FIG. 6

0	1	0
1	-4	1
0	1	0

FIG. 7

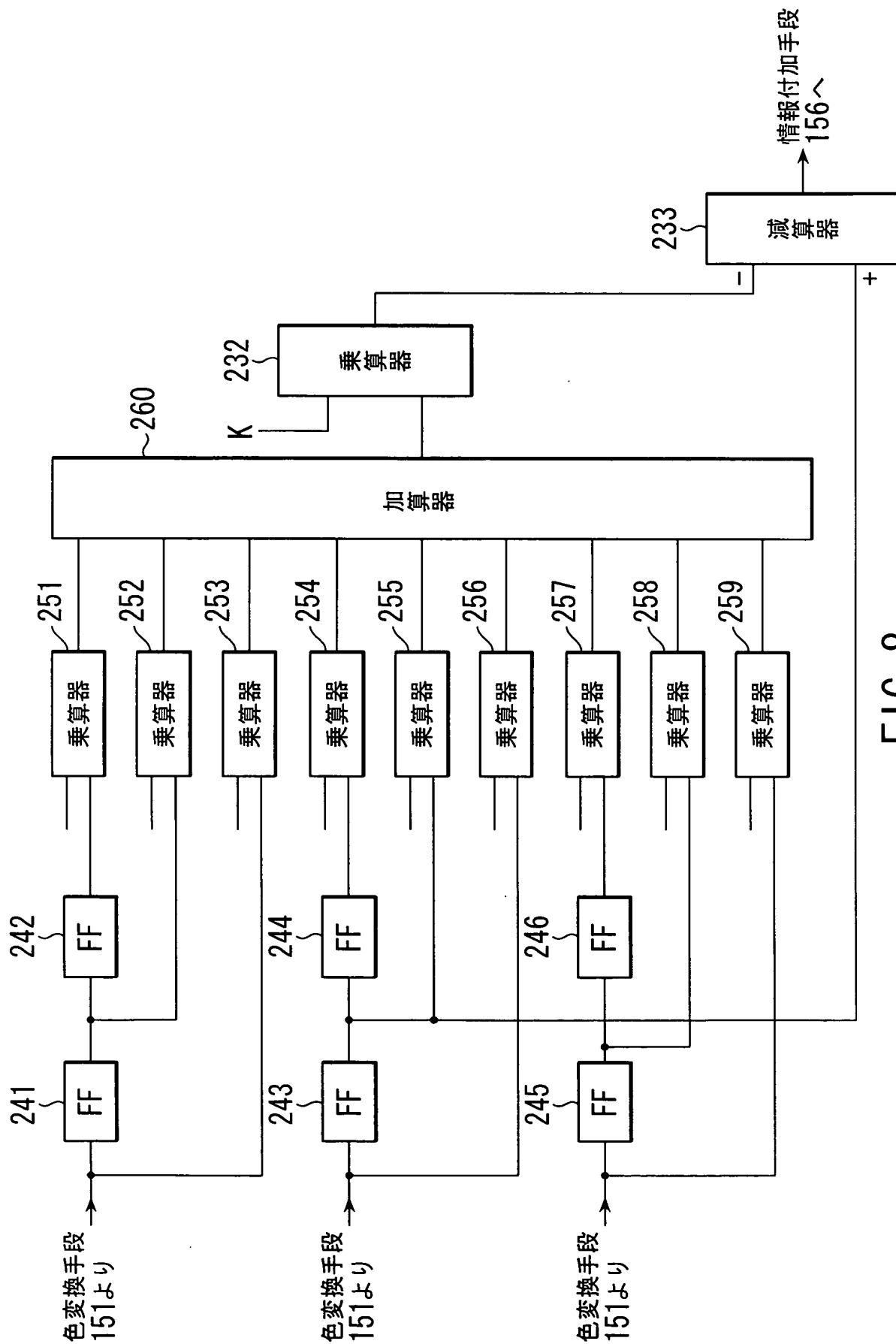


FIG. 8

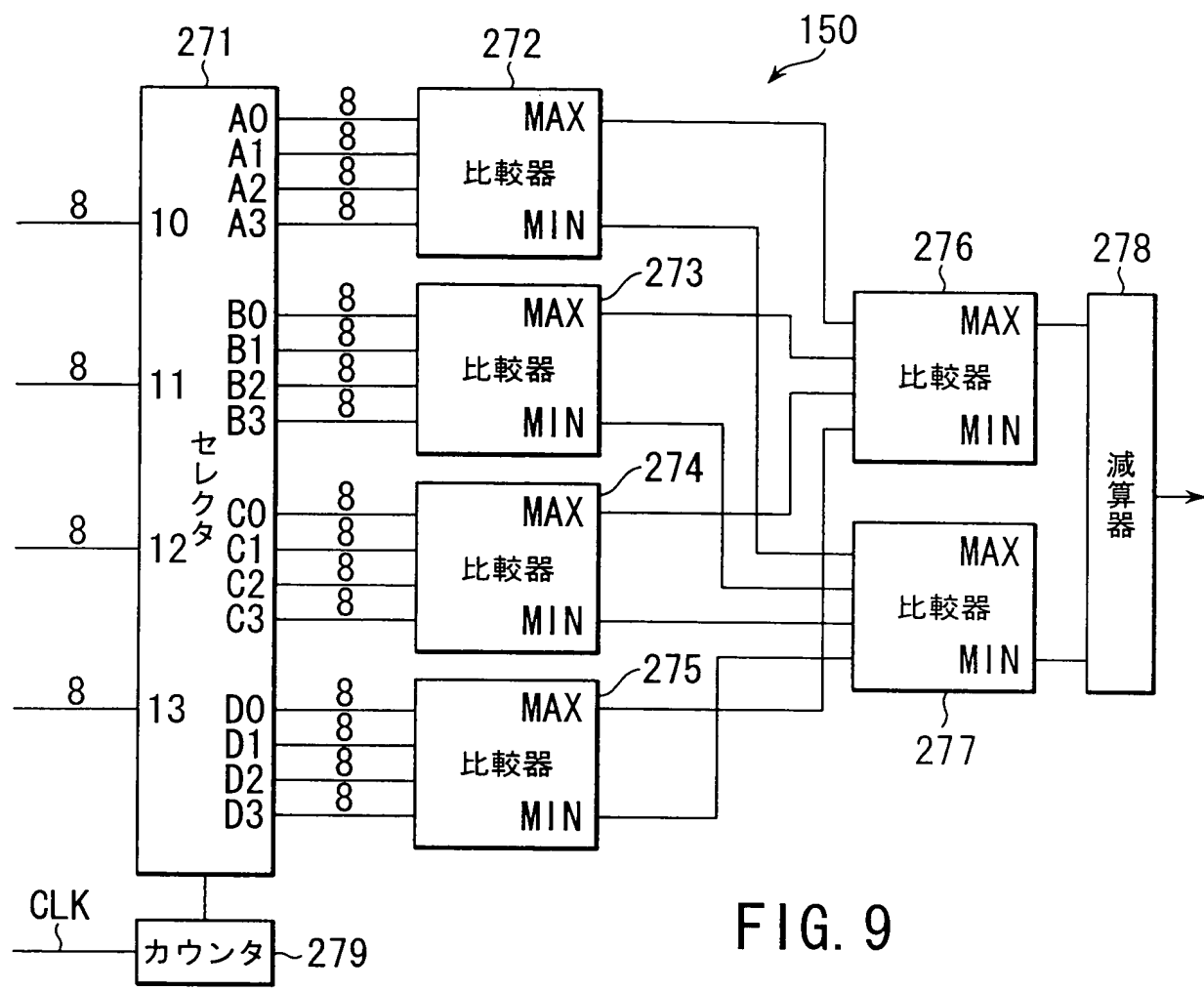


FIG. 9

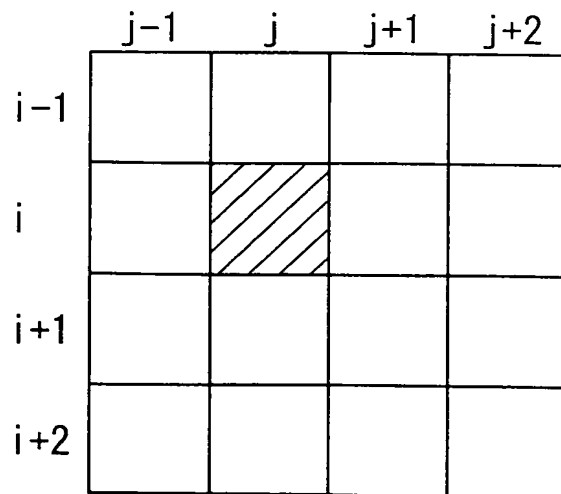


FIG. 10



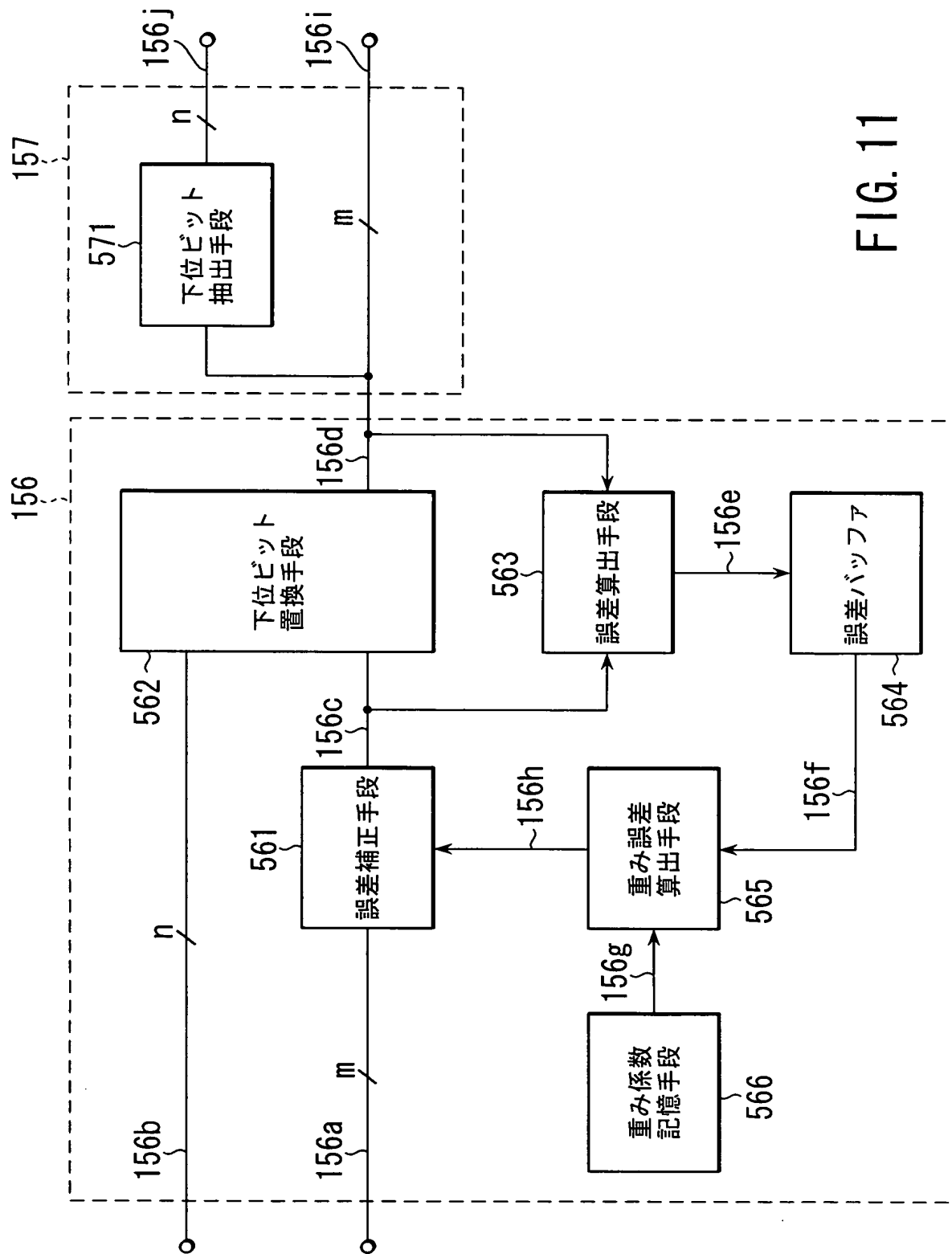


FIG. 11

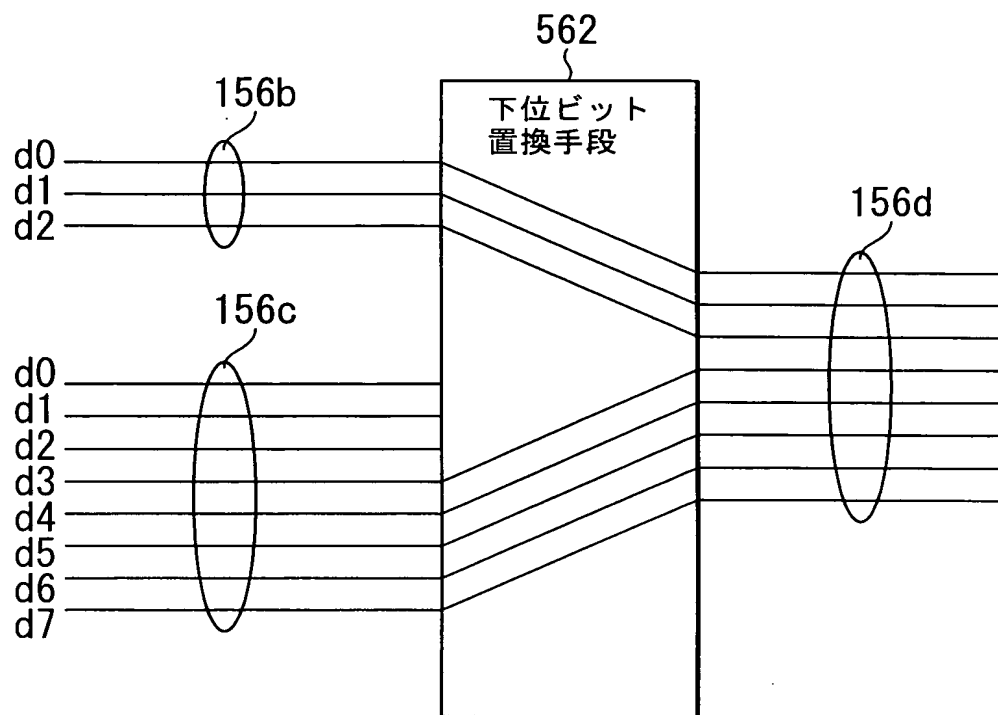


FIG. 13

	*	A
D	C	B

$$A=7/16$$

**B=1/16**

**C=5/16**

$$D=3/16$$

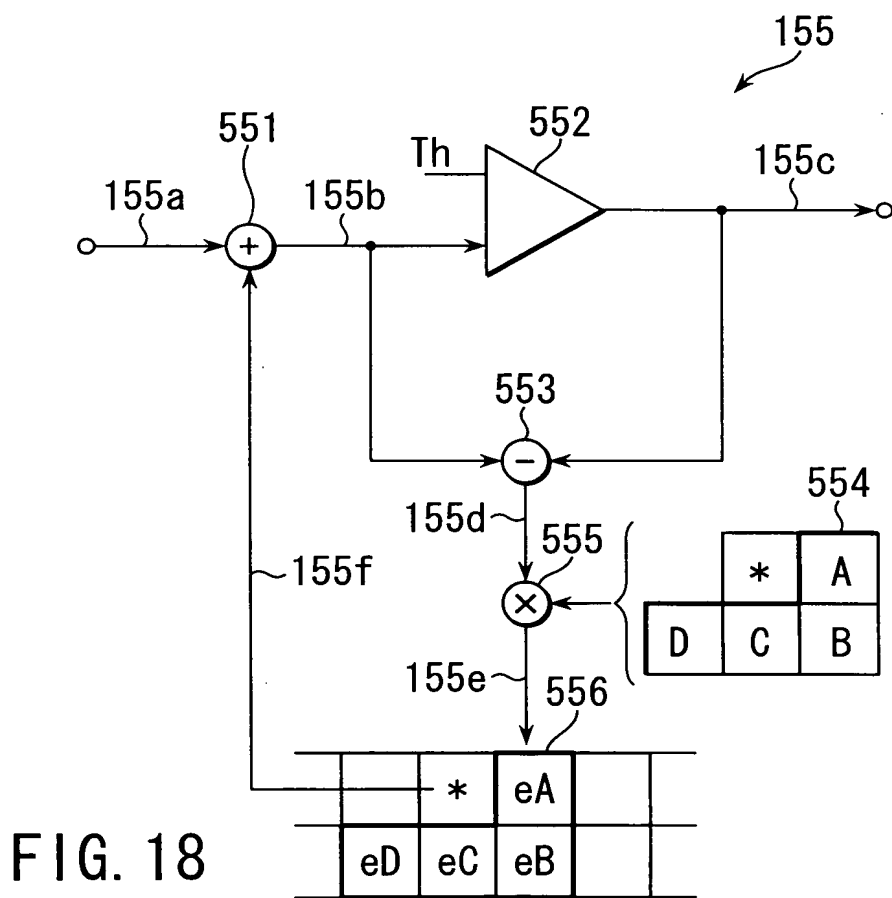
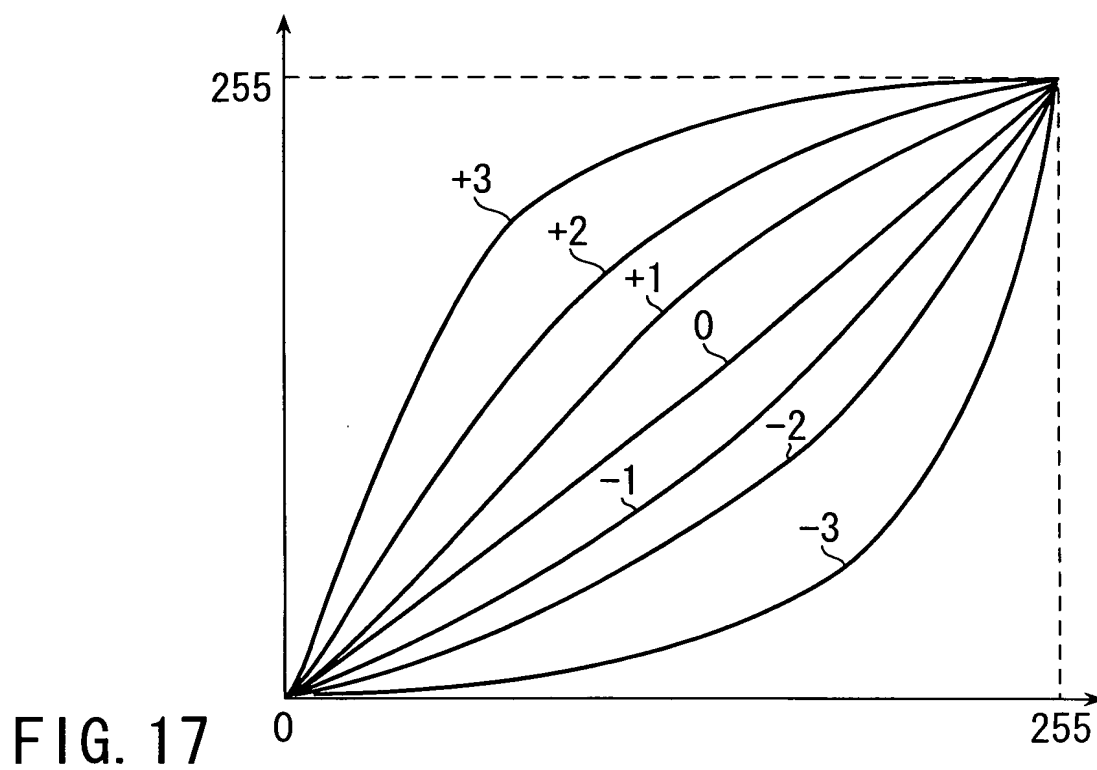
FIG. 14

*	A
---	---

**A=1**

Figure 1 is a block diagram of a control circuit 153. The circuit includes two comparators (281, 283) and two selectors (282, 284). Inputs C, M, and Y are connected to the comparators and selectors. The outputs of the comparators are connected to the selectors. The outputs of the selectors are connected to three subtractors (285, 286, 287), which produce outputs C', M', and Y' respectively. A constant K is also connected to the subtractors.

FIG. 16





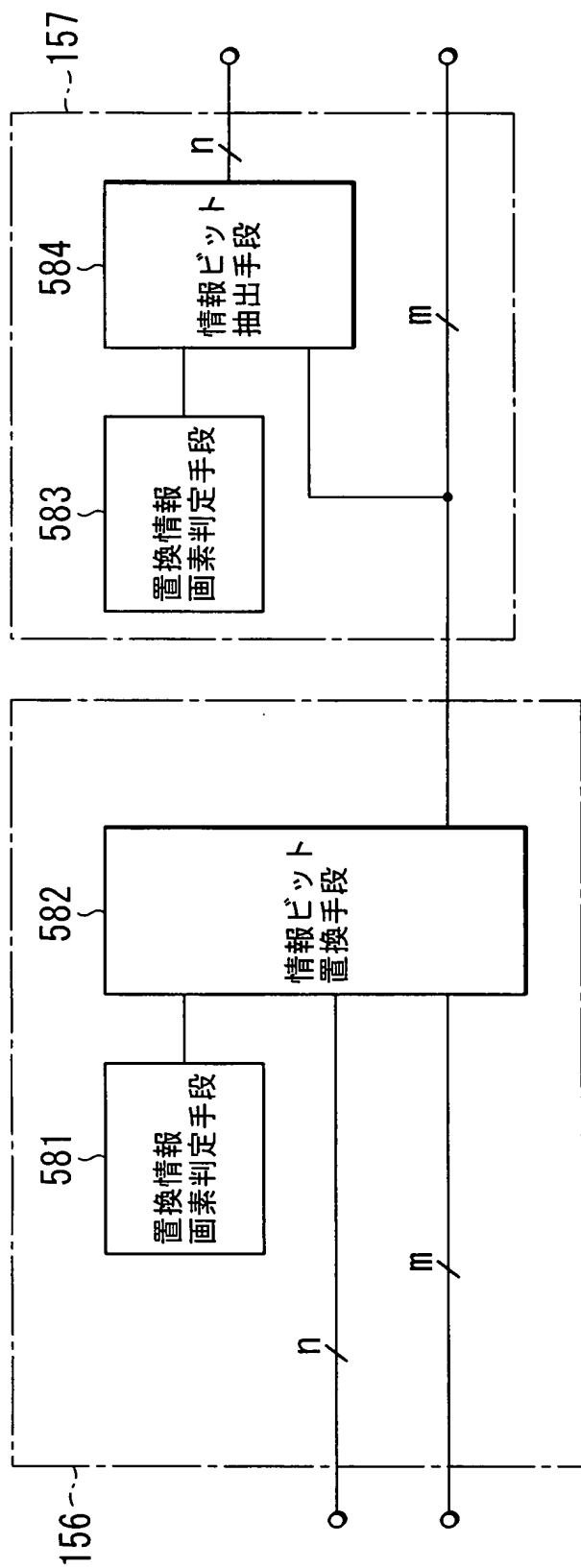
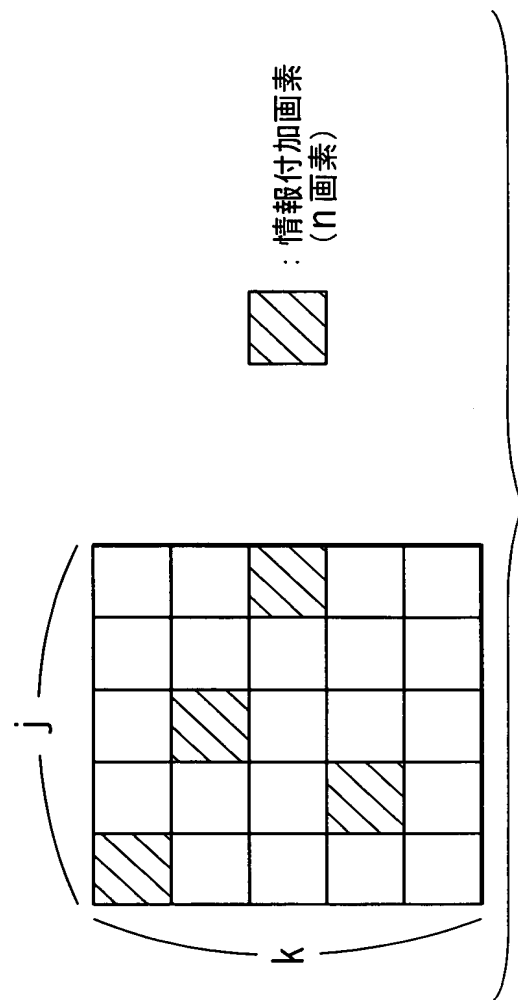


FIG. 20



**FIG. 21**



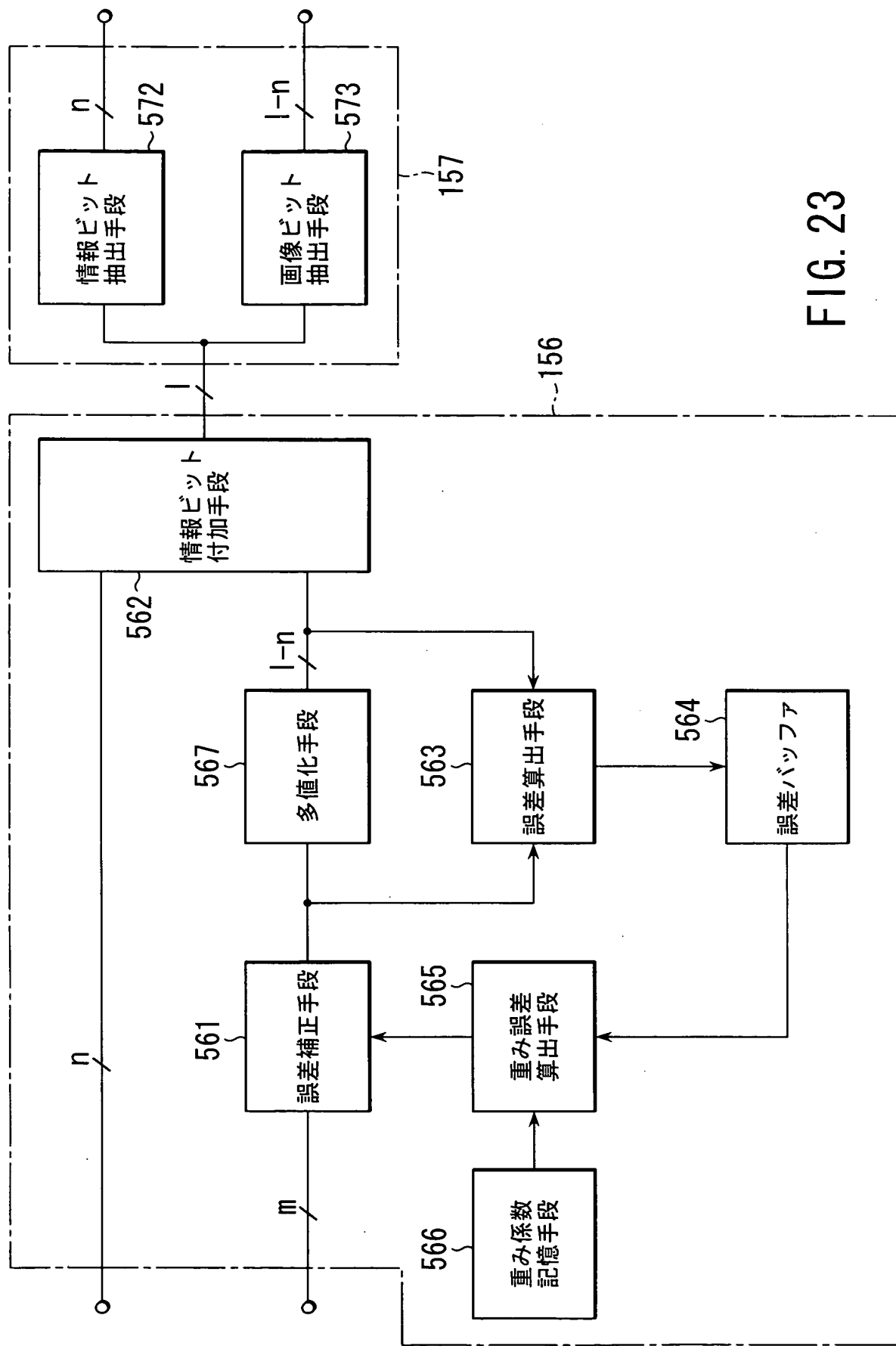


FIG. 23





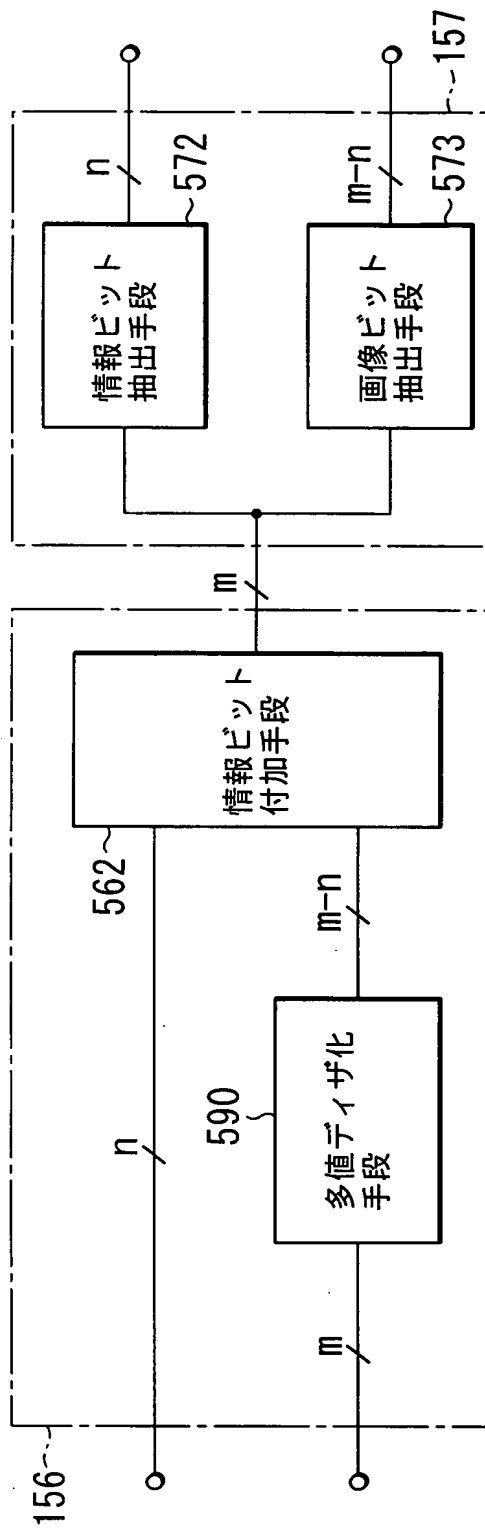


FIG. 25

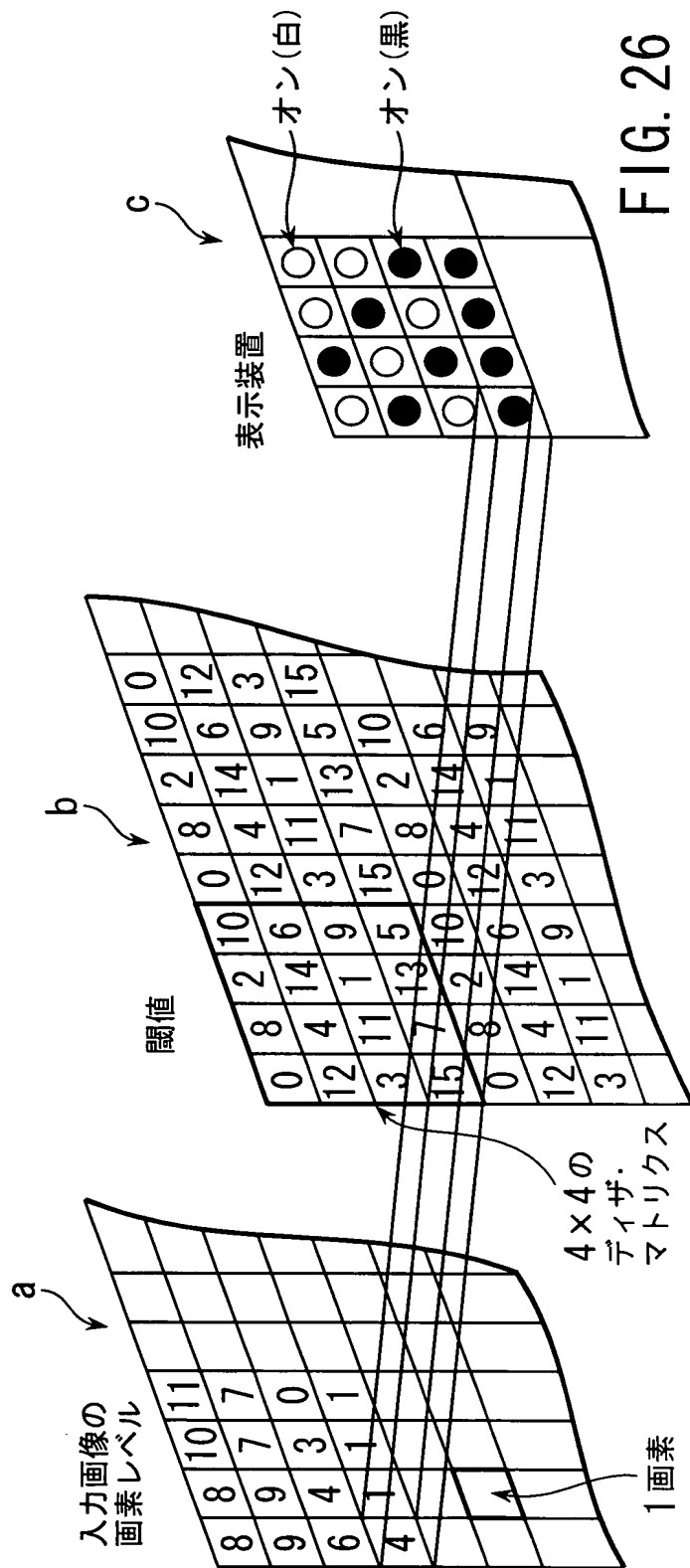


FIG. 26

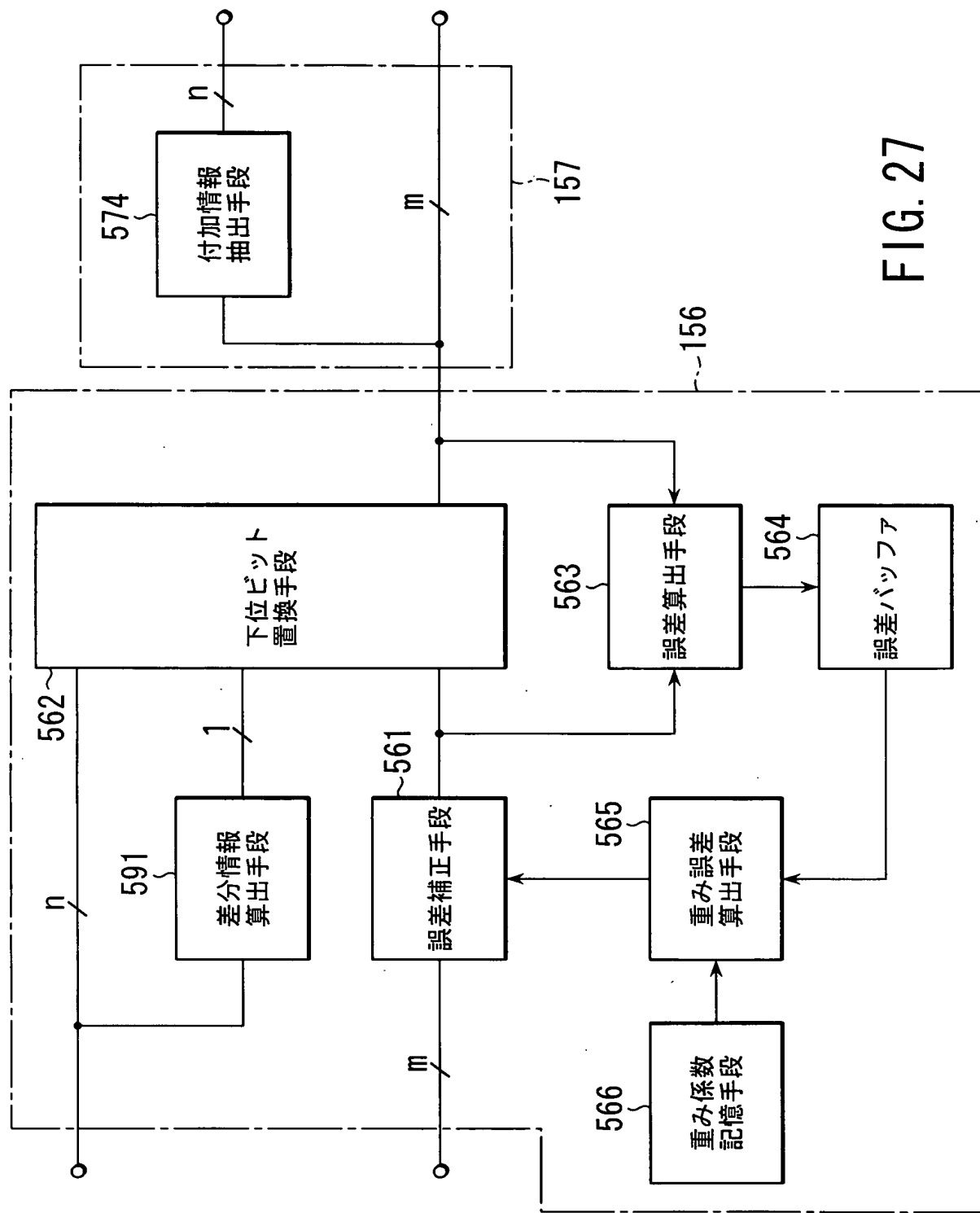


FIG. 28 { 差分 > 0      m:8      n:3

MS8      LS8

The diagram shows a horizontal register divided into eight segments. The first four segments are white, and the last four segments are filled with diagonal hatching. Above the register, the text '差分 > 0' is on the left, 'm:8' is above the first four segments, and 'n:3' is above the last three segments. To the left of the register is a large curly brace labeled 'FIG. 28'. To the right of the register is the label 'i'.

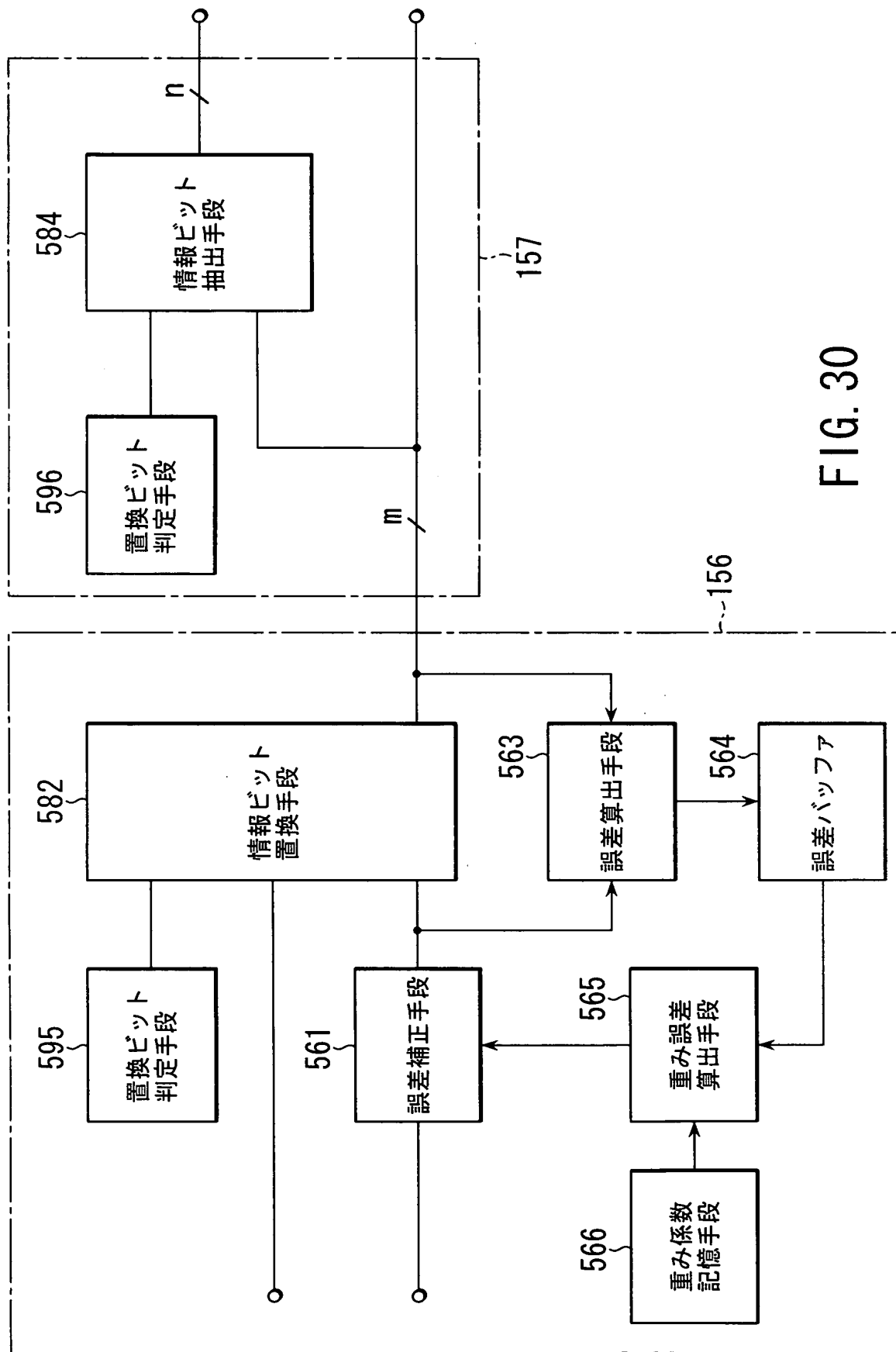


FIG. 30